

FIG. 1 is a block diagram of a system 10 for transmitting and receiving data. The system 10 includes a Transmitter 10 and a Receiver 20. The Transmitter 10 includes a Stream Partitioning System 15, a Stream Prioritization System 14, a Scheme Selection System 16, and a Control System 18. The Receiver 20 includes a Scheme detection system 22. Encoded data 12 is input to the Transmitter 10. The Transmitter 10 outputs data to the Receiver 20.

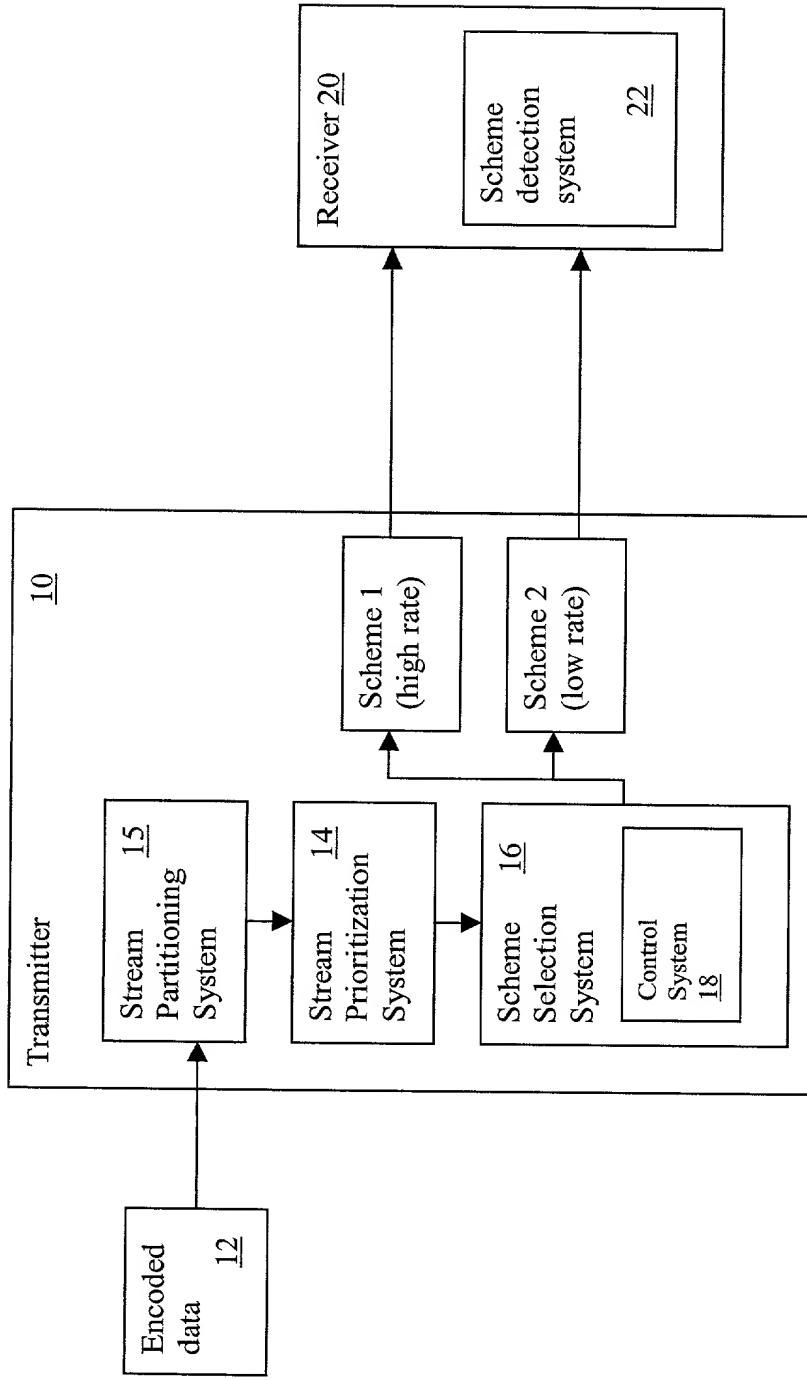


FIG. 1

FIG. 2 is a block diagram of a video encoding and decoding system. The system includes an encoder 30 and a decoder 50. The encoder 30 receives video data 32 and processes it through a data encoding system 40, a stream partitioning system 33, a stream prioritization system 34, and a scheme selection system 36. The scheme selection system 36 includes a control system 38. The encoder 30 outputs two schemes: Scheme 1 (high rate) and Scheme 2 (low rate). The decoder 50 receives these schemes and uses a scheme detection system 52 to select the appropriate scheme for decoding, resulting in decoded data 44.

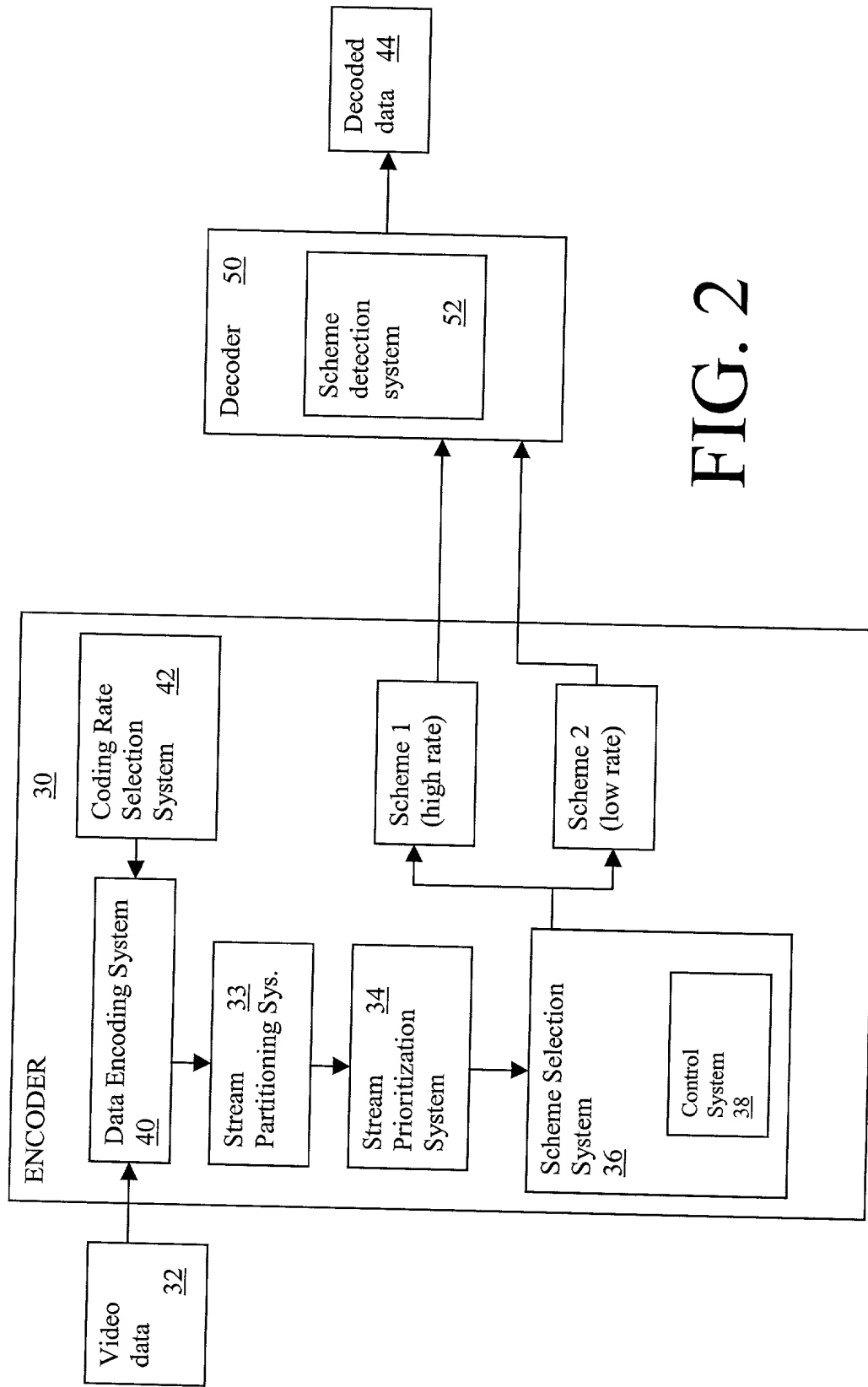


FIG. 2